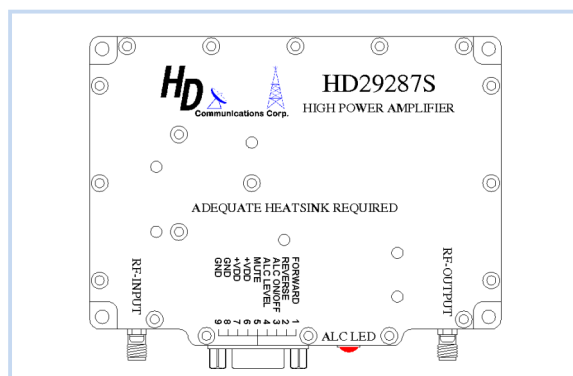


Product Features

- Broadband Design High power RF amplifier
- LDMOS Solid State power amplifier
- High linearity and high IP3
- 50 ohms input / output impedance



Electrical Specification

Parameter	Specification	Remark
Frequency Range	1800MHz~2200MHz	400MHz BW
Output Power(Psat)	+20W min.	CW 1 Tone
Output Power(P1dB)	+15W min.	CW 1 Tone
Gain	42dB $\pm$ 1dB	
Gain Flatness	$\pm$ 1.5dB	
Gain Variation Over Temp.	$\pm$ 1dB max.	
Harmonic	-40dBc	
IMD @ 35 dBm	< -40dBc	CW 2 tone, Ch Sp. : 100KHz
Input VSWR	1.5:1 max.	
Output VSWR	1.5:1 max.	Isolator Applied
Normal Operating Voltage	+28 $\pm$ 2Vdc	
DC Current Consumption	3.0A max.	43dBm
Enable	TTL Low Active	
Output Detect	+4.0V @Po=+20W	0.1V/dB
Noise Figure	10dB max.	

Environmental Specification

Parameter	Specification	Remark
Operating Temperature	0°C~+50°C	
Storage Temperature	-40°C~+85°C	
Relative Humidity	5%~95%	Non-condensing

Mechanical Specification

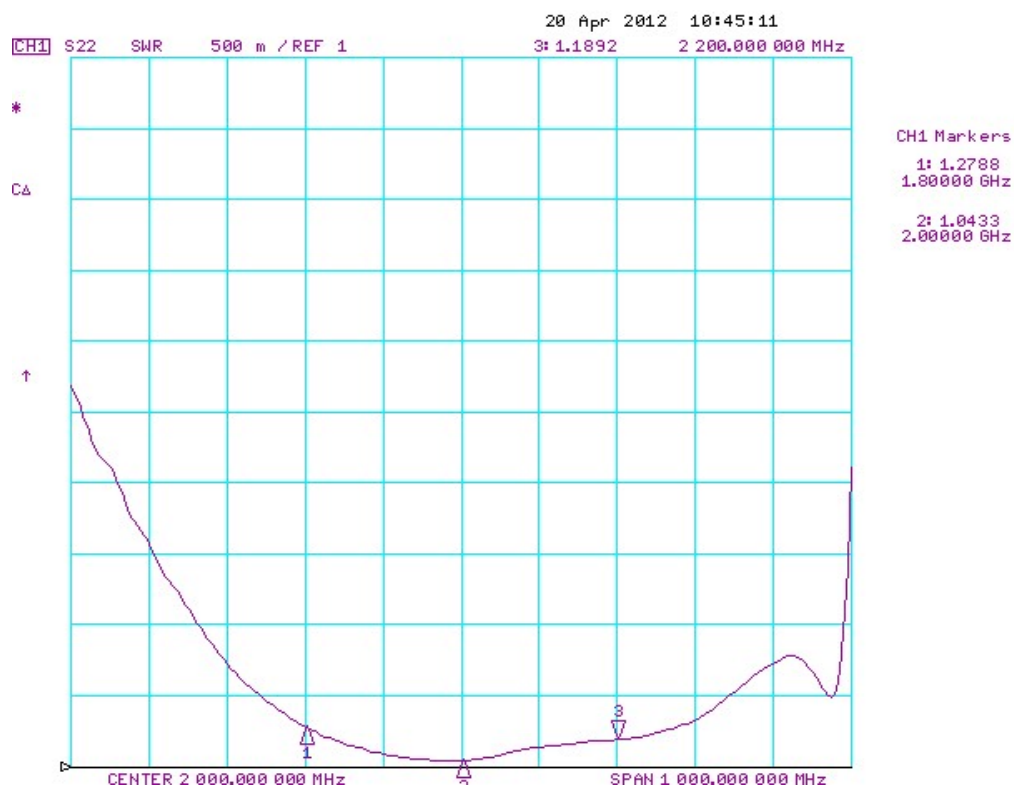
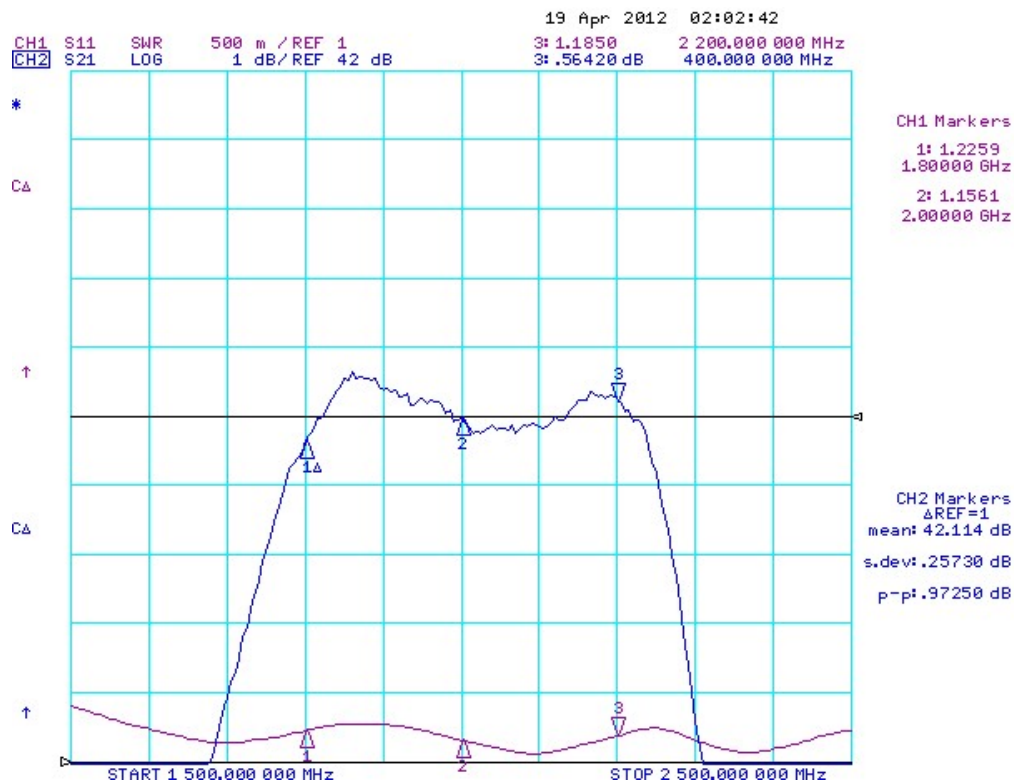
Physical Dimension : 5.0 Inch X 3.75 Inch X 1.0 Inch

Pin Assignment

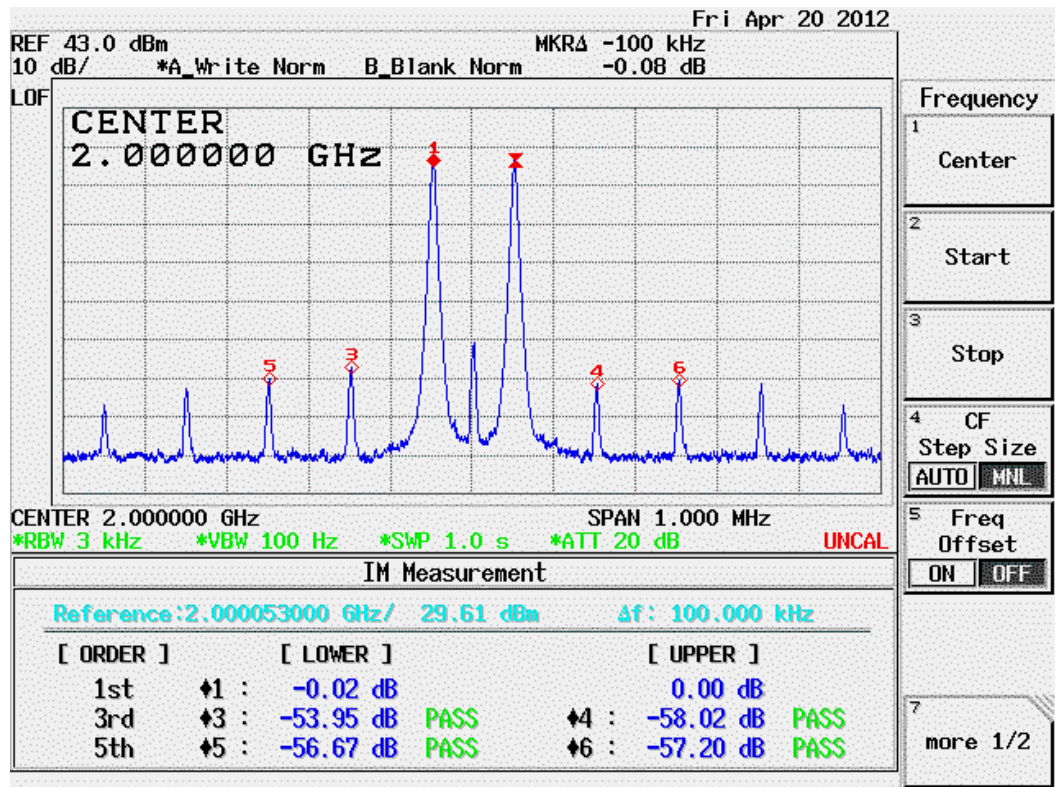
I/O Interface (D-Sub 9Pin Male)	1. Forward	4.0V@Po=+20W, 0.1V/dB
	2. Reverse	4.0V@Po=+20W, 0.1V/dB
	3. ALC On/Off	ALC On : TTL Low, ALC Off : TTL High
	4. ALC Level	35-43dBm adjustable via 0-5Vdc Analog levels
	5. Mute	Enable=TTL Low & Open Disable=TTL High
	6,7 VCC	+28±2Vdc
	8,9 GND	Ground

LED	LED Indicator	Output Power level indicator referenced to ALC setting
------------	---------------	--

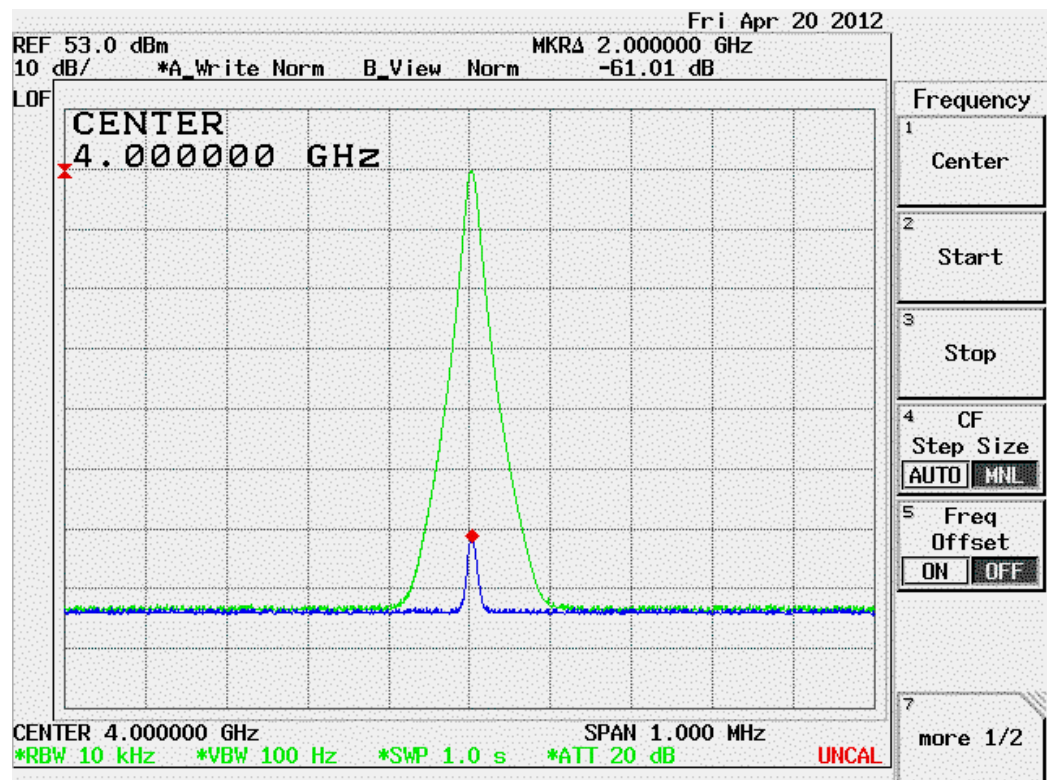
Electrical Test Data
Gain & In / Output VSWR



IMD @Po=+35dBm, 0.9A



Harmonic



Outline Drawing

