

Solid State General Communication Power Amplifier

HD29213

400 – 485 MHz / 25 Watts

- Solid-state linear design
- Small form factor and lightweight
- Suitable for CW, SMR, TETRA applications
- 50 ohm input/output impedance
- High reliability and ruggedness
- Built in Output Isolator
- Built in monitoring circuit



ELECTRICAL SPECIFICATIONS @ +28 VDC, 25°C, 50 Ω System

Parameter	Symbol	Min	Typ	Max	Unit
Operating Frequency	BW	400		485	MHz
Output Power CW	P _{CW}		40		Watt
Output Power @ 1 dB Gain Compression Point	P _{1dB}	25			Watt
Small Signal Gain	G _{SS}	46	-	48	dB
Small Signal Gain Flatness	ΔG		±0.25	±0.5	dB
Third Order Intercept Point 2-Tones, P _{OUT} = 2 W/Tone, Δ = 25 – 500 KHz	IP3		+54		dBm
Input/Output Return Loss	S ₁₁ / S ₂₂			-14 / -10	dB
Noise Figure	NF		7	10	dB
Harmonics @ P1 dB Gain Compression Point	H			-40	dBc
Spurious Signals	Spur		-70	-60	dBc
Operating Voltage	V _{DC}	26	28	30	Volt
Supply Current @ P _{OUT} = 25 W CW	I _{DD}		3.0		Amp
Supply Current @ P _{OUT} = 4 W Composite	I _{DD}		1.6	2.0	Amp

MECHANICAL SPECIFICATIONS

Parameter	Value	Units	Limits
Dimensions	5.0 x 3.75 x 1.0	Inch	Max
Weight	1.0	lb.	Max
RF Connectors In/Out	SMA Female		
DC Connectors	D-sub, 9 Pins, Male		
Cooling	External Heatsink		

ENVIRONMENTAL CHARACTERISTICS (Design to Meet)

Parameter	Symbol	Min	Typ	Max	Unit
Operating Case Temperature	T _c	-10		+50	°C
Storage Temperature	T _{stg}	-40		+85	°C
Relative humidity (non-condensing)	RH			95	%

PROTECTIONS

Input Overdrive		+6 dBm		Max
Over Power Shutdown		45 dBm		Min
Load VSWR @ 25 W		∞ @ all load phase & amplitude		Nom
Thermal Overload		85°C shutdown		Max

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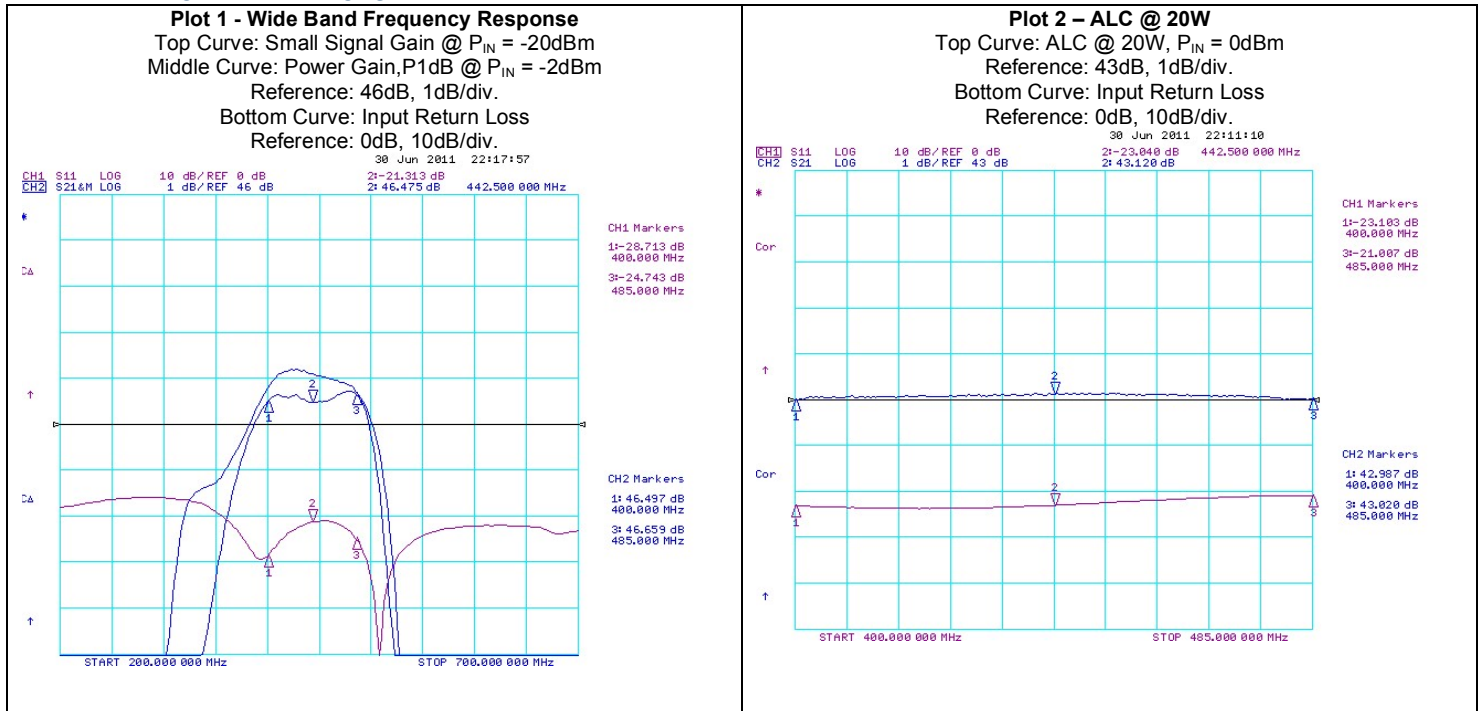
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INTERFACE CONNECTORS

D-Sub, 9-Pin

Pin #	Description	Specifications
1	Forward Power Monitor	Continuous Analog voltage relative to forward power via RMS detector FWD _M : 13 – 43 dBm @ 0 – 5 V (100 mV/dB) 28 dBm output = $V_{FWD} = 2.5 V_{DC}$
2	Reverse Power Monitor	Continuous Analog voltage relative to reflected power via RMS detector REV _M : 13 – 43 dBm @ 0 - 5V (100 mV/dB) 28 dBm output = $V_{REV} = 2.5 V_{DC}$
3	ALC ON/OFF	ALC ON = TTL "Low" ALC OFF = TTL "High"
4	ALC Level	Continuous adjustable range via analog input levels Setting Point (ASP): 33 – 44 dBm @ 0 – 5 V (100 mV/dB) Error Range (AER): ±1.5 dB Response Time (ART): 100 mS/dB
5	Mute	Amplifier Enable: TTL "Low" or Open Amplifier Disable: TTL "High"
6	+VDD	+28 $V_{DC} \pm 2 V$
7	+VDD	+28 $V_{DC} \pm 2 V$
8	GND	Ground
9	GND	Ground
LED	LED Indicator	Output Power level indicator referenced to ALC setting (Independent of ALC ON or OFF)

TYPICAL PERFORMANCE PLOTS



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OUTLINE DRAWING

