

**20-512MHz 350W Class A/AB  
High Performance Amplifier  
with High Speed Disable**

- ❖ Class A/AB 350W linear amplifier
- ❖ Fast output disable,  $\leq 1\mu\text{sec}$
- ❖ VVA, over 30dB range
- ❖ Analog temperature out
- ❖ High temperature alarm
- ❖ Independent MMIC disable
- ❖ Output stage current sense
- ❖ Temperature compensated bias
- ❖ SMA input, N output connectors



The HD31583 is a high gain, high power Class A/AB amplifier module designed for military and commercial communications systems. It features fast output disable with  $\leq 1\mu\text{sec}$  response time, and includes numerous control and monitoring functions. It exhibits excellent gain flatness and linearity, and is offered in a compact housing with a nickel plated copper base for best possible thermal performance.

**Specifications** (Two power supplies are required.)

$V_{\text{supply1}} = +28\text{VDC}$ ,  $I_{\text{DQ}} = 3.3\text{A}$ ,  $V_{\text{supply2}} = +50\text{VDC}$ ,  $I_{\text{DQ}} = 0.8\text{A}$   
 $P_{\text{out}} = 350\text{W}$ ,  $T_{\text{base}} = 50^\circ\text{C}$ ,  $Z_{\text{load}} = 50\Omega$

Parameter	Min	Typ	Max	Units
Freq. Range	20		512	MHz
$P_{1\text{dB}}$	350	See Fig. 4		W
Input Power		-2.3	+0.7	dBm
Gain	54.7	57.7		dB
Gain Flatness		+/-0.6	+/-1.2	dB
Drain Current, 28VDC		3.3	3.5	A
Drain Current, 50VDC		13.2	14.7	
Efficiency (output stage only)	48	53		%
IRL		-23	-14	dB
$f_2$		-46	-34	dBc
$f_3$		-21	-10	dBc
$\text{IMD}_3$ 350W PEP, $\Delta f=100\text{kHz}$		-36	-28	dBc
Dimensions	4.50 X 8.40 X 1.35 (114.30 X 213.36 X 34.29)			inch (mm)

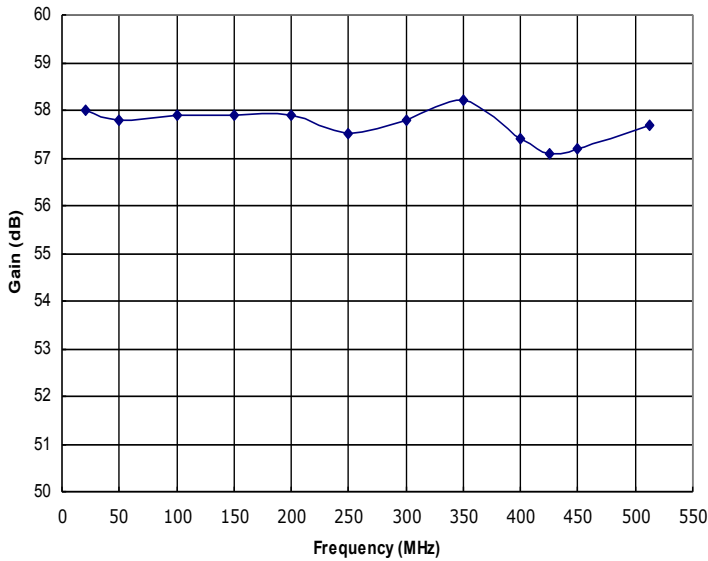
**Maximum Ratings**

Operation beyond these ratings may damage amplifier.

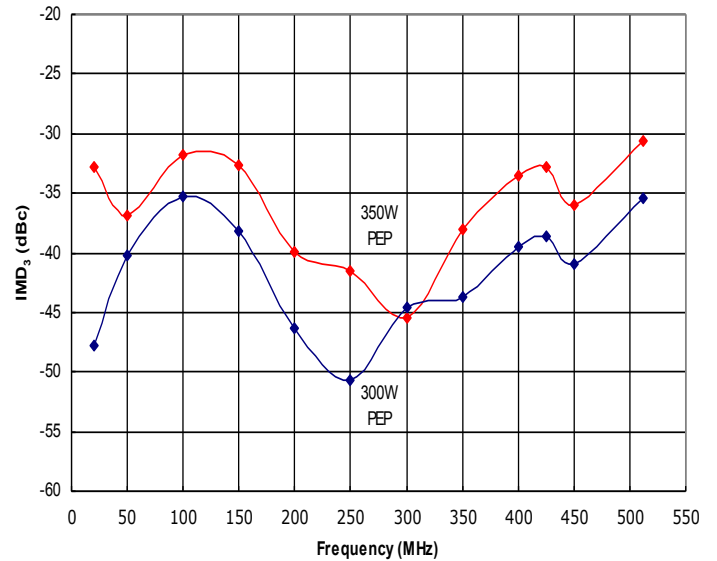
Parameter	Value
$V_{\text{supply1}}$	24-28VDC
$V_{\text{supply2}}$	46-50VDC
Bias Current, 28VDC	3.5A
Bias Current, 50VDC	1.0A
Drain Current, 28VDC	3.5A
Drain current, 50VDC	15A
Load Mismatch*	3:1
Housing Base Temperature (See important notes on cooling, Page 5.)	60°C
Storage Temperature	-40°C to 85°C

\*All phase angles, 350W forward power, current limited to 15A for 5 seconds maximum.

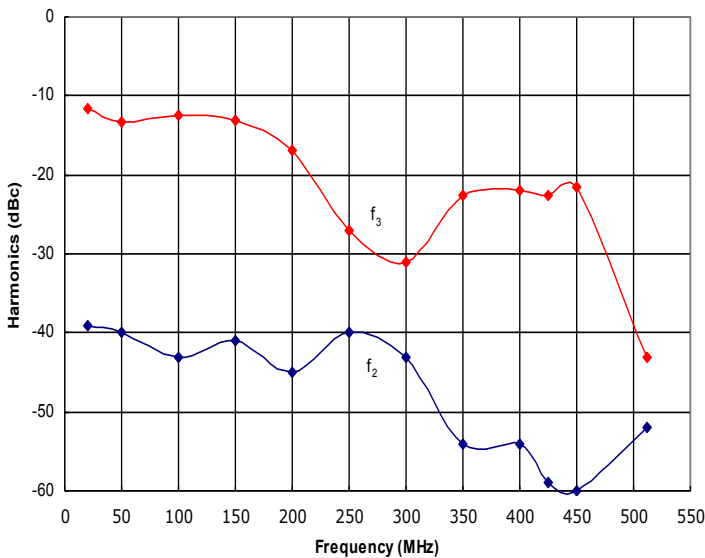
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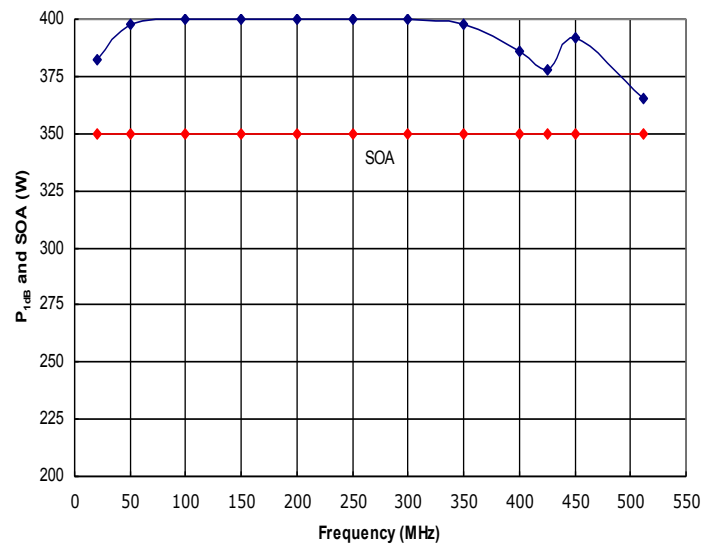
**Figure 1: HD31583 Typical Gain @  
 $P_{out} = 350W$ .**



**Figure 2: HD31583 Typical  $IMD_3$  @ 350W  
and 300W PEP,  $\Delta f = 100kHz$ .**

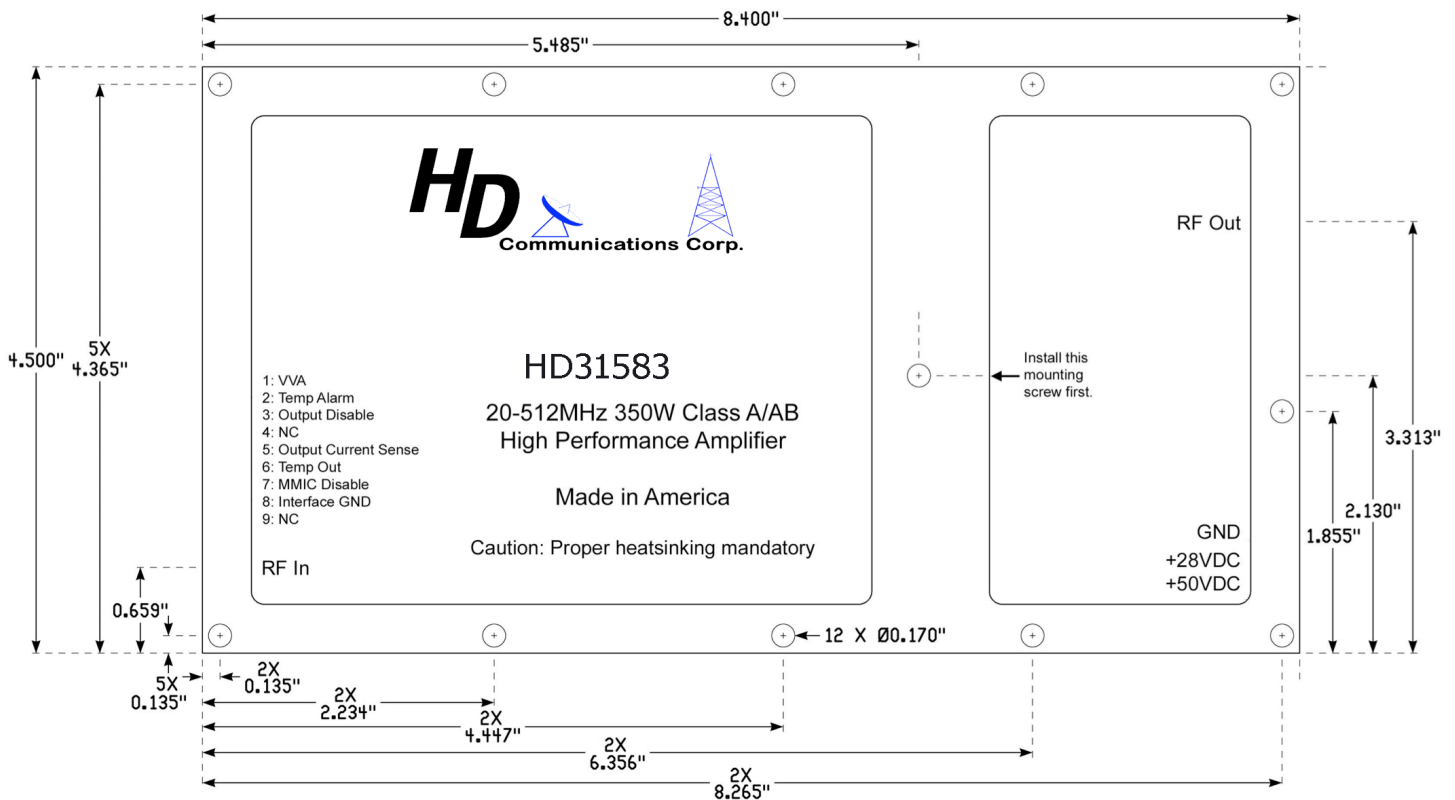


**Figure 3: HD31583 Typical  $f_2$  and  $f_3$  @  
 $P_{out} = 350W$ .**

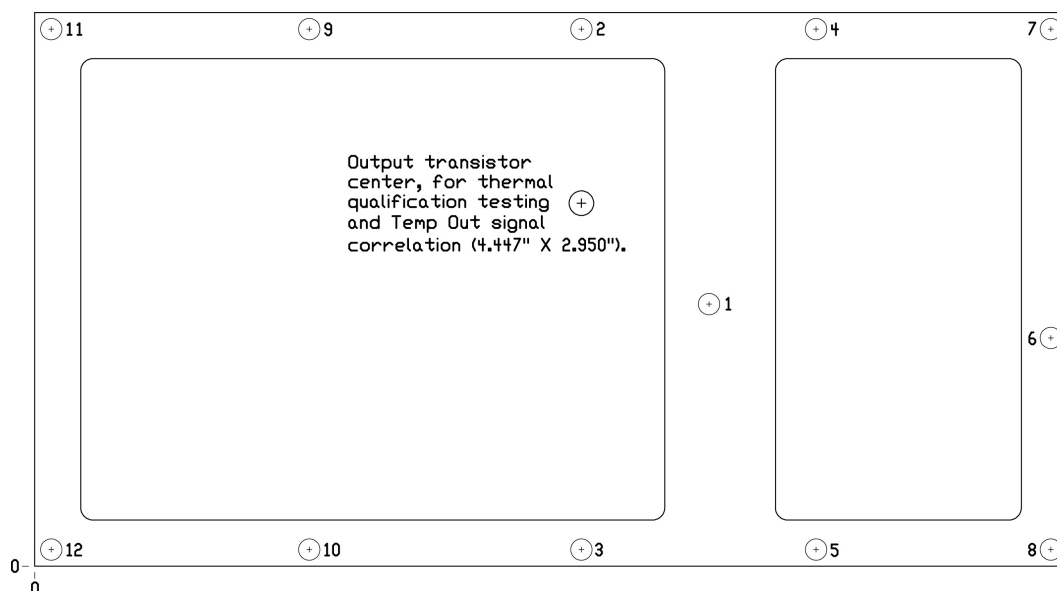


**Figure 4: HD31583 Typical  $P_{1dB}$  and Safe  
Operating Area (SOA). The amplifier is  
capable of delivering more power than it is  
safe to generate. **Do not exceed the SOA  
shown above without first contacting  
HD COMMUNICATIONS CORP. to discuss your  
application.****

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**Amplifier Mounting Hole and RF Locations, and Mounting Screw Tightening Sequence**





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## Interface Pins and Functions

- 1: VVA** The voltage variable attenuator has a slope of approximately 14dB/volt, from 1.4 to 3.6VDC. Maximum attenuation is reached by 4.4VDC. The acceptable control range is 0 to 5.0VDC. Do not exceed 5.0VDC on this pin. *The VVA control voltage should be set to zero volts during amplifier power up and power down.*
- 2: Temp Alarm** This indicates an over-temperature condition, and is set to trip at a housing base temperature of approximately +60°C. It is pulled up internally to +5VDC, and will pull down to 0V at +60°C. This signal exhibits 5°C of hysteresis, and will reset to +5VDC at approximately +55°C.
- 3: Output Disable** This is active high at +5VDC, and is TTL compatible. It disables the output and its two drive stages simultaneously, and will reduce full rated output power to near zero\* in  $\leq 1\mu\text{sec}$ . When set to 0VDC, the amplifier will return to within 0.25dB of full power in approximately 50 $\mu\text{sec}$ . For further RF burst fidelity improvements, the end user may solder an electrolytic capacitor to the +50VDC feedthru cap and ground stud next to it. HD Communications Corp. recommends a value of  $\geq 4700\mu\text{F}$ .
- \* Due to feedback paths inherent in all stages of the amplifier, there will be a miniscule amount of RF still present on the output even when bias has been disabled. For additional RF quieting, the VVA may be set to maximum attenuation simultaneously with the Output Disable signal.
- 4: NC** This pin has no internal connection and should be left floating.
- 5: Output Current Sense** This is a DC signal scaled at 0.30V/A, and monitors the output transistor *only*. Over-current protection should be enabled at >4.50VDC, or 15A output transistor current.
- 6: Temp Out** This is an analog signal that is a nominal +0.75V at +25°C. The temp monitor IC has a positive slope of 10mV/°C with increasing temperature. However, a temperature gradient and an offset will exist between the temperature reported on this pin and the actual housing base temperature directly under the output transistor (see bottom figure on Page 3). The reported slope will be closer to 6.7mV/°C, rather than the native 10mV/°C of the IC. It is up to the end user to properly characterize this signal's response with their chosen cooling configuration.
- 7: MMIC Disable** This is a supplementary disable pin, for additional amplifier quieting at the user's discretion. It exhibits approximately a 50 $\mu\text{sec}$  delay for enable, and a 30 $\mu\text{sec}$  delay for disable. Due to the internal biasing resistor in the MMIC, there will be significant RF bleedthrough even when the MMIC is disabled.
- 8: Interface GND** All control and monitoring signals should be referenced to this pin. It is tied internally to the amplifier ground plane. A minimum of 20 gauge wire is recommended for best grounding.
- 9: NC** This pin has no internal connection and should be left floating.



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**Instructions for Amplifier Use**

- 1) **IMPORTANT:** This amplifier uses a single high power output transistor which will dissipate up to 375W even into a well matched 50 ohm load. Peak dissipation will occur between 400 and 450MHz, and very careful attention **must** be paid to proper amplifier cooling or transistor lifetime will be substantially reduced and will not be covered under warranty. The maximum allowed housing base temperature directly under the center of the output transistor is 60°C (see bottom figure on Page 3 for thermal qualification point location). Due to amplifier cooling requirements, a high performance thermal compound (Wakefield Type 122 or better) **must** be used. Standard performance thermal compounds will be inadequate in air cooled systems, and their use will void the warranty. Apply the thermal compound to the bottom of the amplifier housing. Thinner is better, but ensure that when mounted to your heatsink, contact across the **entire** module base is made. Gaps and air bubbles will significantly reduce cooling, leading to possible amplifier damage. Use 12 #6-32 or M4 screws to mount the amplifier to your heatsink. [Refer to the figure on the bottom of Page 3 for the proper order in which to torque the mounting screws.](#)
- 2) Guarantee sufficient airflow through the heatsink fins to keep the maximum housing base temperature at or less than 60°C. Contact HD COMMUNICATIONS CORP. for details on how to qualify your heatsink's performance, if needed.
- 3) Connect a proper signal source to the RF In connector, and desired load to the RF Out connector. Torque connectors to industry standards for the types supplied with the amplifier.
- 4) Connect +50VDC to the bottom feedthru connector on the output end of the amplifier housing. Use a minimum of 14 gauge wire (12 gauge preferred). Connect +28VDC to the top feedthru connector on the output end of the amplifier housing. Use a minimum of 18 gauge wire. Ensure that the DC supply voltages are within the ranges specified in the Maximum Ratings section. It is preferred to ground the amplifier through the heatsink for lowest impedance ground. In addition, a safety ground should be connected to the ground stud on the output end of the amplifier housing. Use a minimum of 12 gauge wire for this connection.
- 5) Apply desired signals/monitoring lines to the interface pins in the DB-9 connector. Refer to the Interface Pins and Functions section for signal descriptions, limits, and timing requirements.
- 6) Apply +50VDC, then +28VDC, then sufficient RF drive to achieve desired output level. The maximum amplifier RF input power is +0.7dBm. [This amplifier is designed to operate in a non-saturated linear mode only. Regardless of the input power, ensure that the Safe Operating Area \(SOA\) power level indicated in Figure 4 is not exceeded, or amplifier damage may occur, and will void the warranty.](#)
- 7) To disconnect the amplifier, first remove the RF drive, then +28VDC, then +50VDC, then the RF connections.

Contact us at [sales@hocom.com](mailto:sales@hocom.com) with any questions, or for special options, testing requirements, and/or operating conditions not specified in this document.

**Document Control**

Revision	Date	Notes
A	8-15-2017	Initial release.